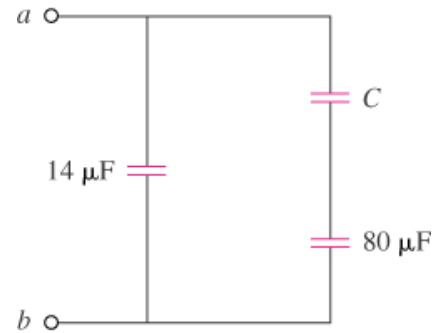


- P1 Concept:** Equivalent capacitance
Find: C in the schematic if the capacitance of the whole circuit (between terminals a and b) is $30\ \mu\text{F}$:
Hint: First digit of answer is 2.



- P2 Concept:** Design problem
Find: Using a single op amp, a capacitor, and resistor of values between $100\ \Omega$ to $100\ \text{k}\Omega$, design a circuit to implement the following equation:

$$v_o = -50 \int_0^t v_i(t) dt \quad \text{Assume } v_o(t=0) = 0\text{V}.$$

- Hint:** Variety of correct answers